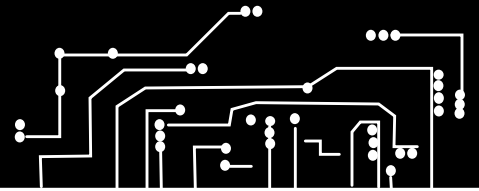


PowerPC Microprocessor Family:



The Programming Environments Manual Errata Sheet

PowerPC Applications
IBM Microelectronics
Austin, Texas

Monday, November 13, 2000

Abstract: This document lists the known errors of the PowerPC Microprocessor Family: The Programming Environments Manual (PEM) dated 3/21/2000.

Page 6-20, Section 6.4, the first paragraph should have the word Table 6-6.

Page 6-21, Table 6-7, line starting with 31 under SRR1 should be moved down, so that it reads, “31 Loaded with equivalent bit from the MSR”

Page 6-27, Table 6-10, line starting with 10-15 under SRR1 should be moved down a line. The bit settings for SRR1 are:

- 10-15 Cleared
- 16-23 Loaded with equivalent bits from the MSR
- 25-27 Loaded with equivalent bits from the MSR
- 30-31 Loaded with equivalent bits from the MSR

Page 7-69 in Section 7.7.6, the figure reference should be to Figure 7-27 which is on page 7-70, not to Figure 7-26.

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